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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-22423

Venkat V. Easwar

Art Unit: 2624

Serial No.: 09/713,446

Examiner: Kyle M. Pendergrass

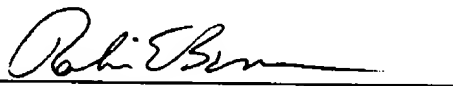
Filed: November 15, 2000

Conf. No.: 3256

For: Multi-Level Dither Screening on a Split Arithmetic Logic Unit Processor

CERTIFICATION OF FACSIMILE TRANSMISSION

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Robin E. Barnum

May 17, 2005
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FACSIMILE COVER SHEET

<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input checked="" type="checkbox"/> AMENDMENT <u>11</u> 10 (10 Pages)
<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT (Page)
<input type="checkbox"/> DECLARATION (# Pages)	<input type="checkbox"/> NOTICE OF APPEAL (# Pages)
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<input type="checkbox"/> CONTINUATION APP'N (# Pages)	
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Venkat V. Easwar	
TITLE OF INVENTION: Multi-Level Dither Screening on a Split Arithmetic Logic Unit Processor	
TI FILE NO.: TI-22423	DEPOSIT ACCT. NO.: 20-0668
FAXED: 5/17/05 DUE: 5/18/05 ATTY/SEC'Y: RDM/reb	
RECEIPT DATE & SERIAL NO.: Serial No.: 09/713,446 Filing Date: November 15, 2000	

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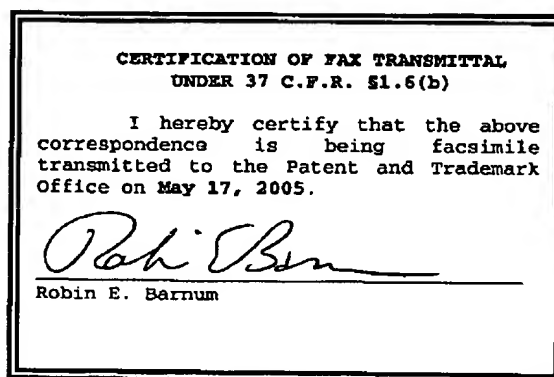
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Easwar
Serial No.: 09/713,446
Filed: November 15, 2000
For: MULTI-LEVEL DITHER SCREENING ON A SPLIT ARITHMETIC LOGIC UNIT
PROCESSOR

Art Unit: 2624
Examiner: Kyle M. Pendergrass
Docket: TI-22423

AMENDMENT UNDER 37 C.F.R. 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450



Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated February 18, 2005. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows: